

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/816,394
Filing Date: 04/01/2004
Applicant: Farbod Aram
Group Art Unit: 2651
Examiner: Dan I. Davidson
Title: WRITE DRIVER SYSTEM FOR DATA STORAGE
SYSTEMS
Attorney Docket: MP0409

Mail Stop Issue Fee
Commissioner for Patents
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Alexandria, Virginia 22313-1450

COMMUNICATION

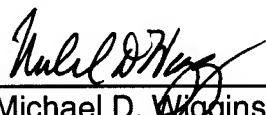
Applicant submitted a Supplemental Information Disclosure Statement on September 23, 2005. The Supplemental Information Disclosure Statement included U.S. Patent Nos. 6,466,091, 6,515,540, and 6,525,589, "Application of the Operational Transconductance Amplifier (OTA) to Voltage-Controlled Amplifiers and Active Filters," 10 pages, "A Five Stage Chopper Stabilized Instrumentation Amplifier Using Feedforward Compensation," 4 pages, "A DC Measurement IC with 130nV_{pp} Noise in 10Hz," 9 pages, "A DC Measurement IC with 130nV_{pp} Noise in 10Hz," Slide Supplement, 5 pages, "700/900mW/Channel CMOS Dual Analog Front-End IC for VDSL with Integrated 11.5/14.5dBm Line Drivers," 10 pages, and "700/900mW/Channel CMOS Dual

Analog Front-End IC for VDSL with Integrated 11.5/14.5dBm Line Drivers," Outline, Overview, 24 pages. As of the date of this communication, it appears that the Examiner has not considered and/or signed off on these documents. Applicant includes herewith a copy of the original Form 1449 that cited the document, as well as copies of the documents. Applicant would like the Examiner to sign off on these references.

Should there be any outstanding matters that need to be resolved in the present application, the Examiner is respectfully requested to contact the undersigned. If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 08-0750 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

Dated: July 21, 2006

By: 
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